

FIG. 1

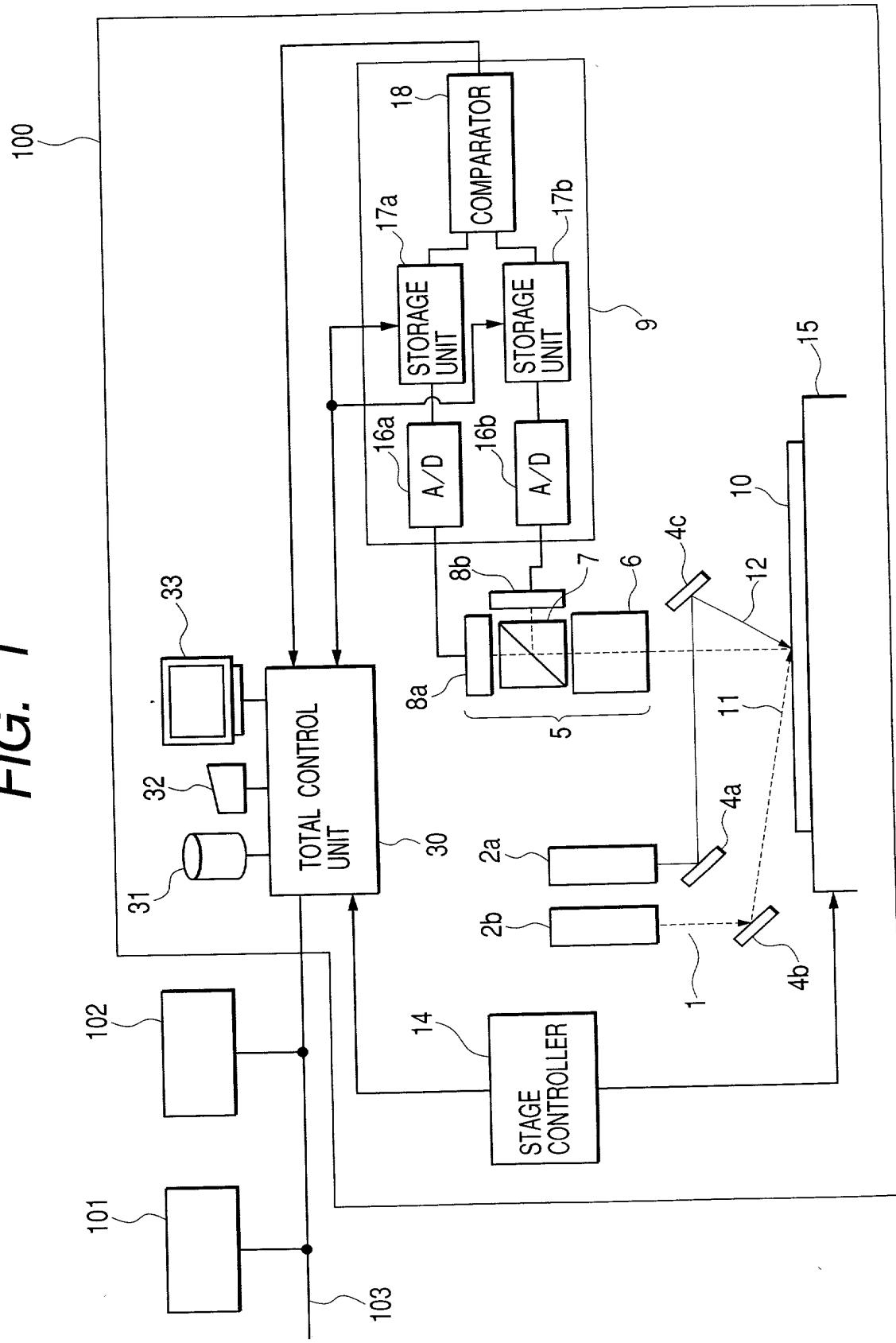


FIG. 2(a)

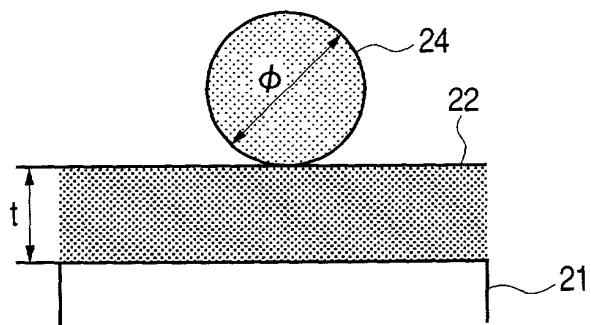


FIG. 2(b)

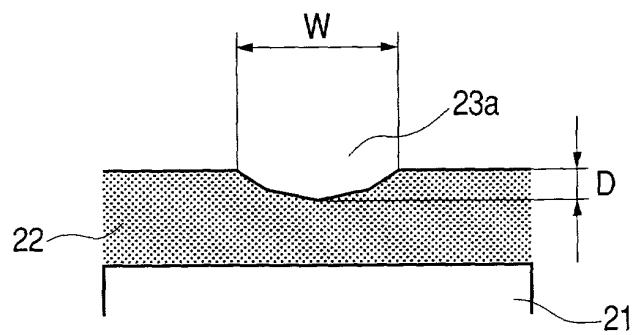


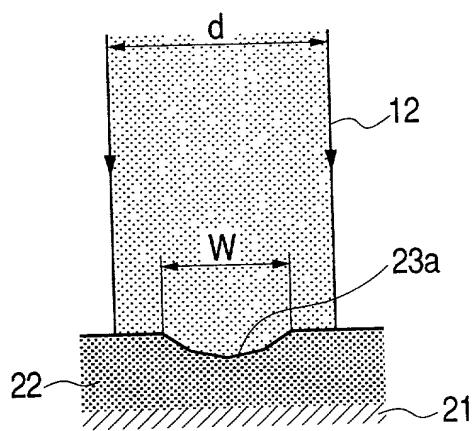
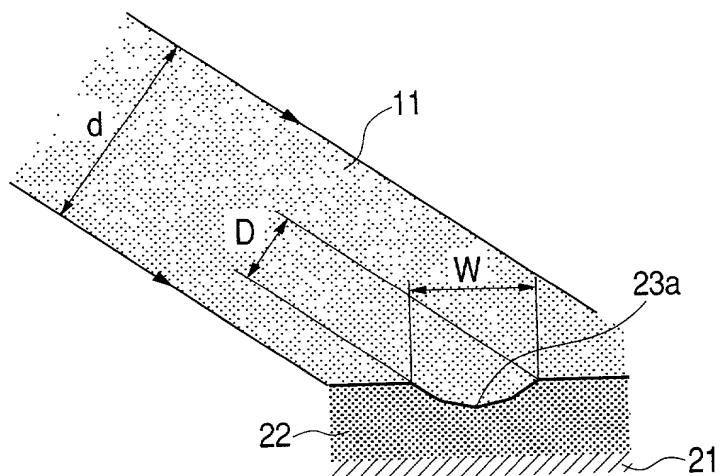
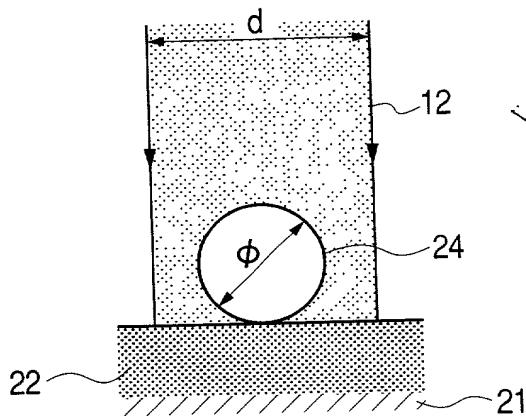
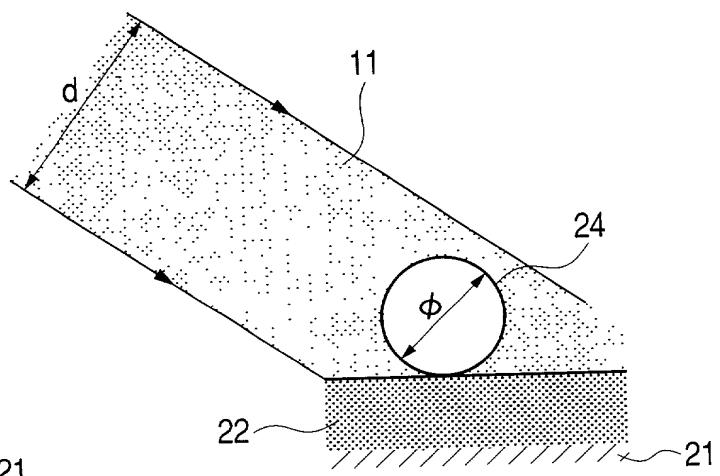
FIG. 3(a)*FIG. 3(b)**FIG. 3(c)**FIG. 3(d)*

FIG. 4

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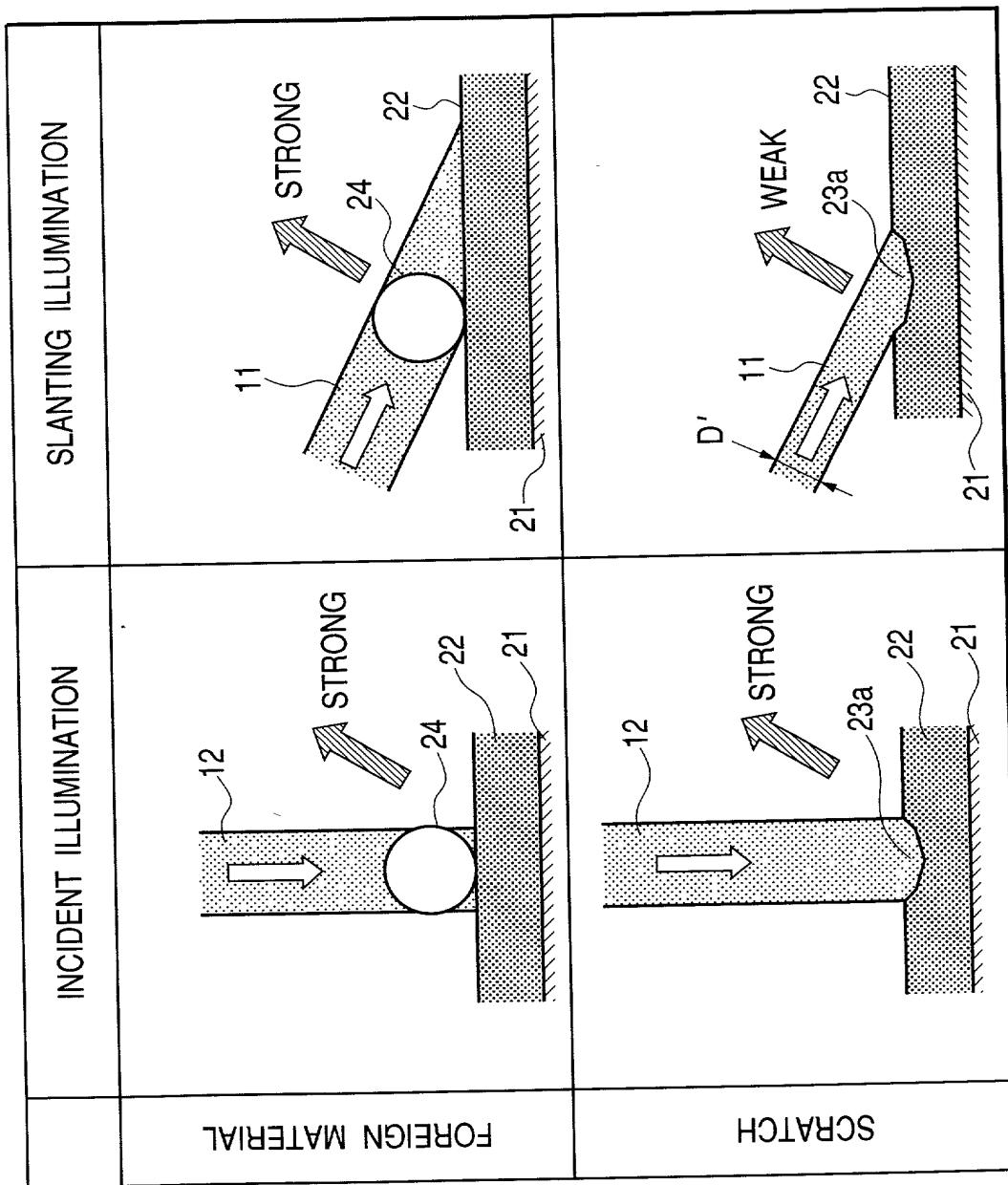


FIG. 5

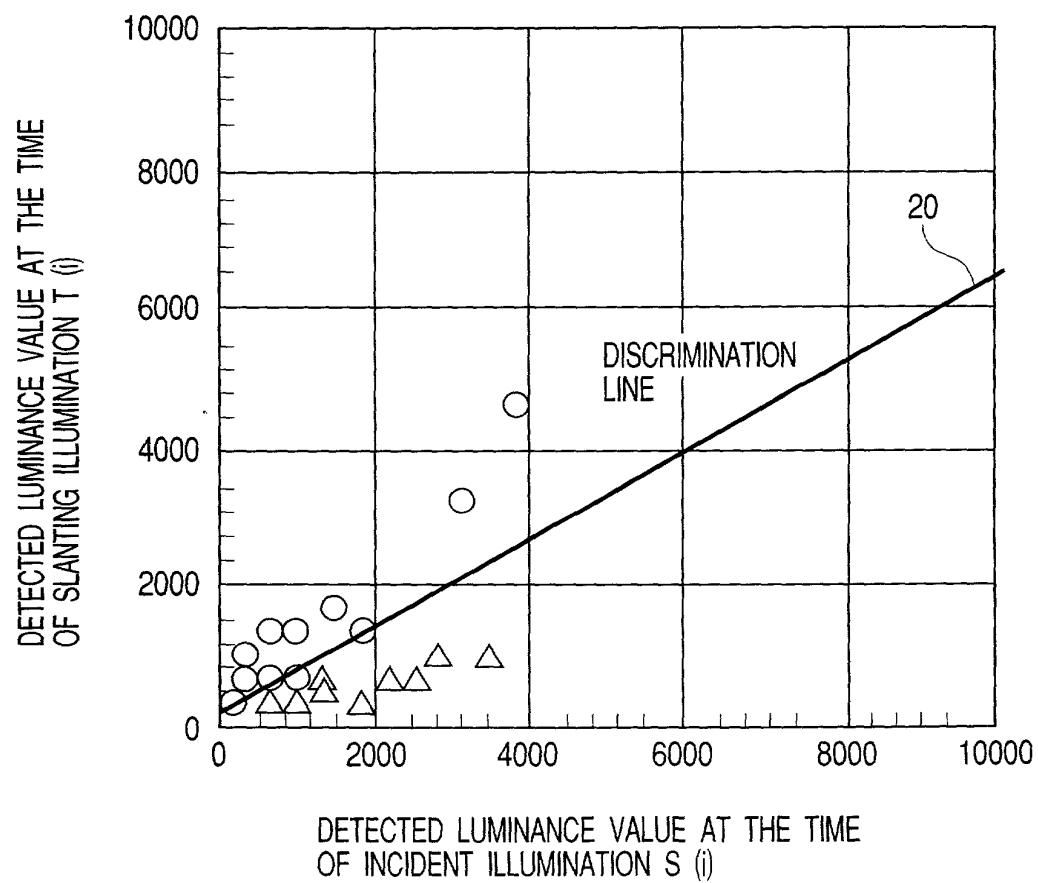


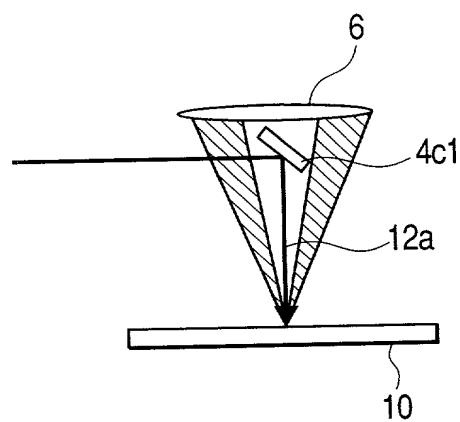
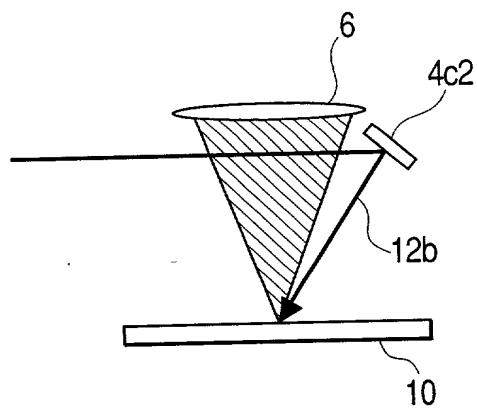
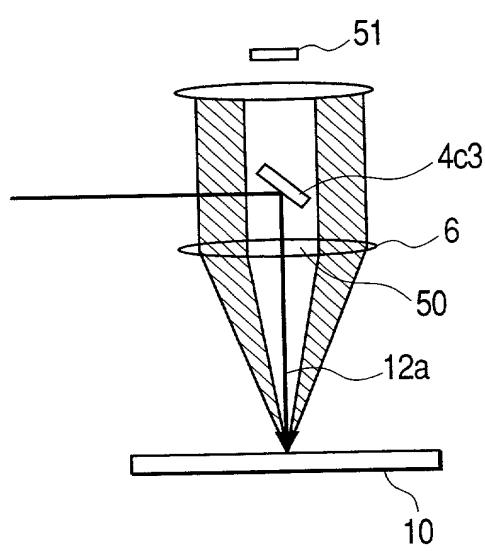
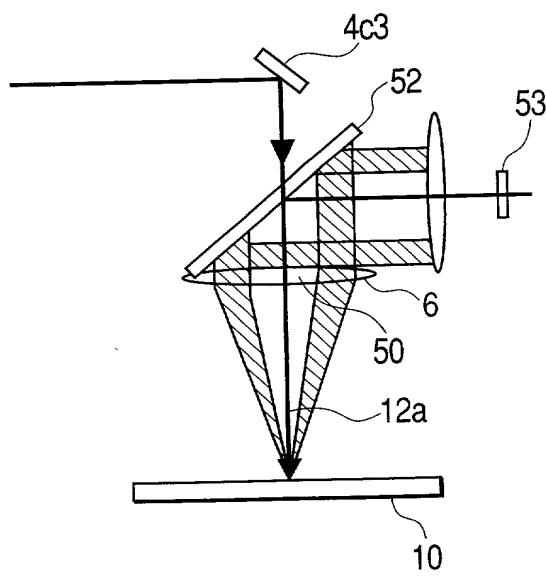
FIG. 6(a)*FIG. 6(b)**FIG. 6(c)**FIG. 6(d)*

FIG. 7(a)

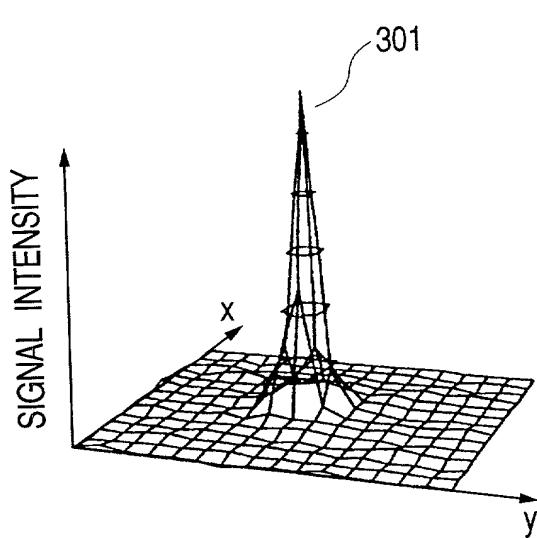


FIG. 7(b)

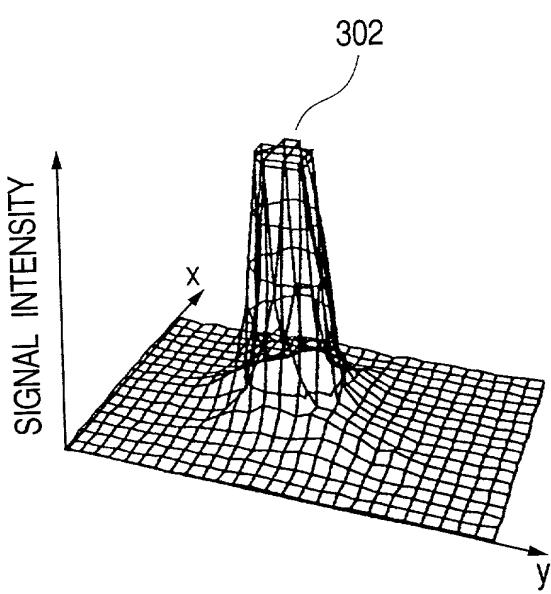


FIG. 7(c)

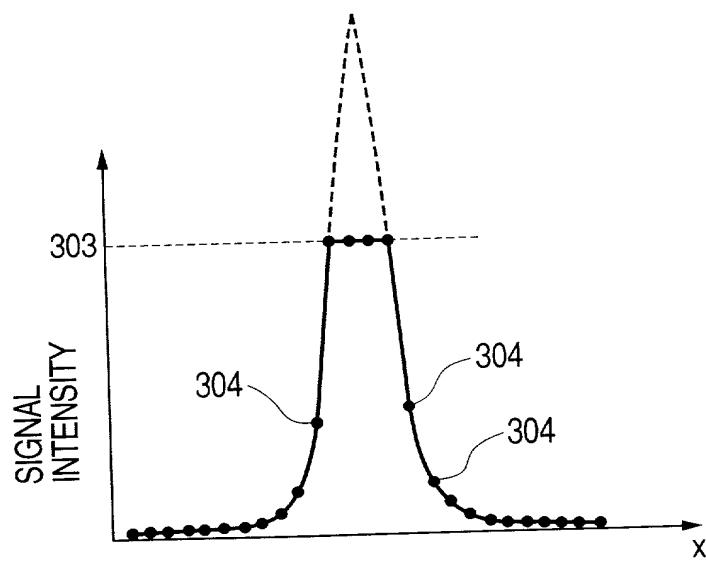


FIG. 8

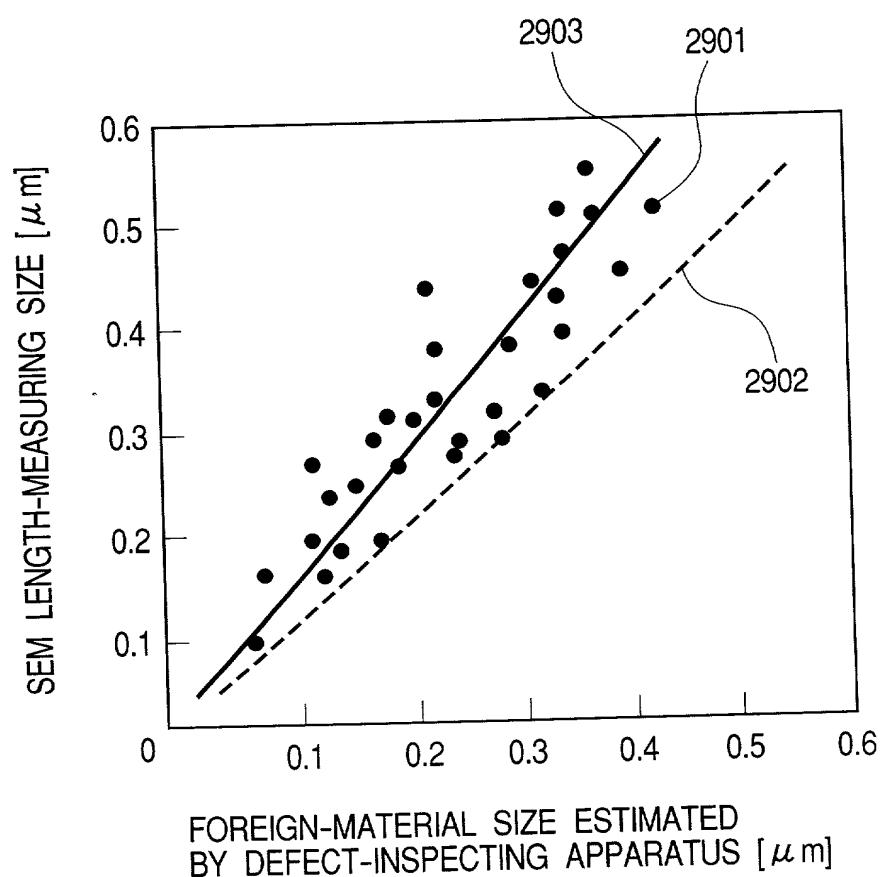
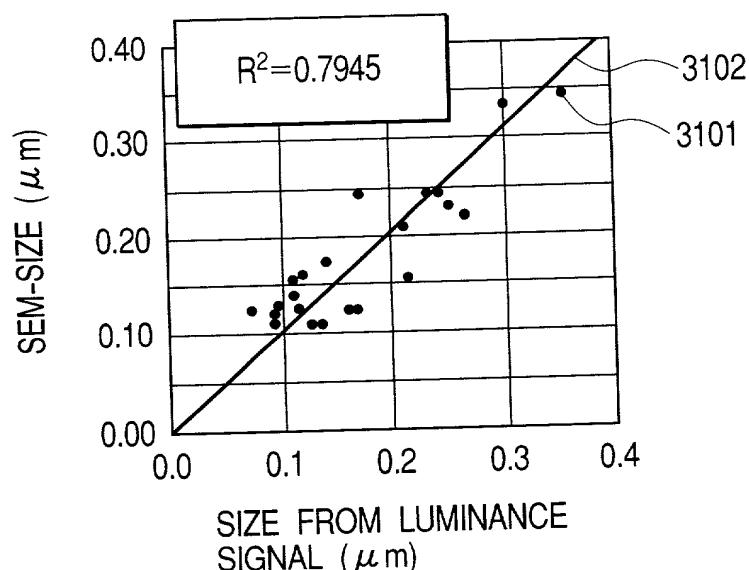


FIG. 9(a)

FRONT-END PROCESS WAFER

**FIG. 9(b)**

BACK-END PROCESS WAFER

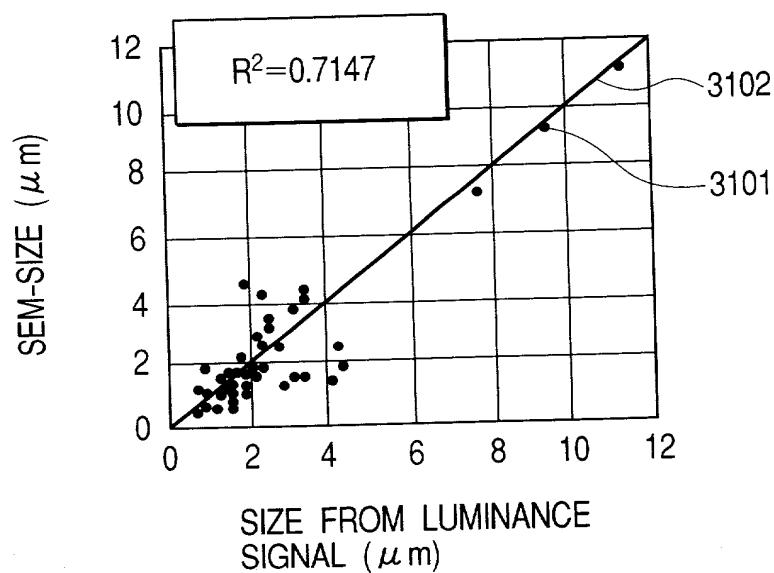


FIG. 10

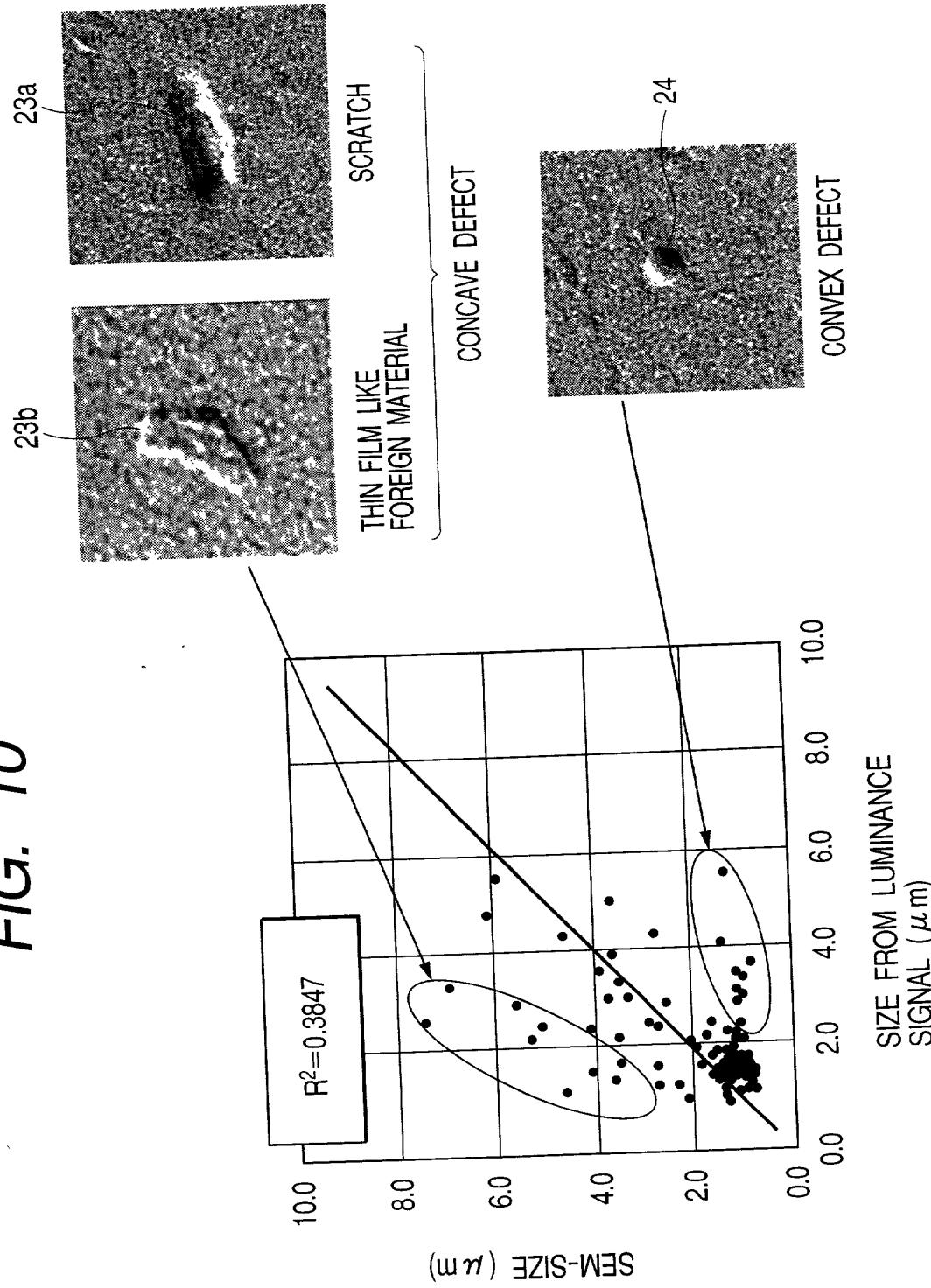


FIG. 11

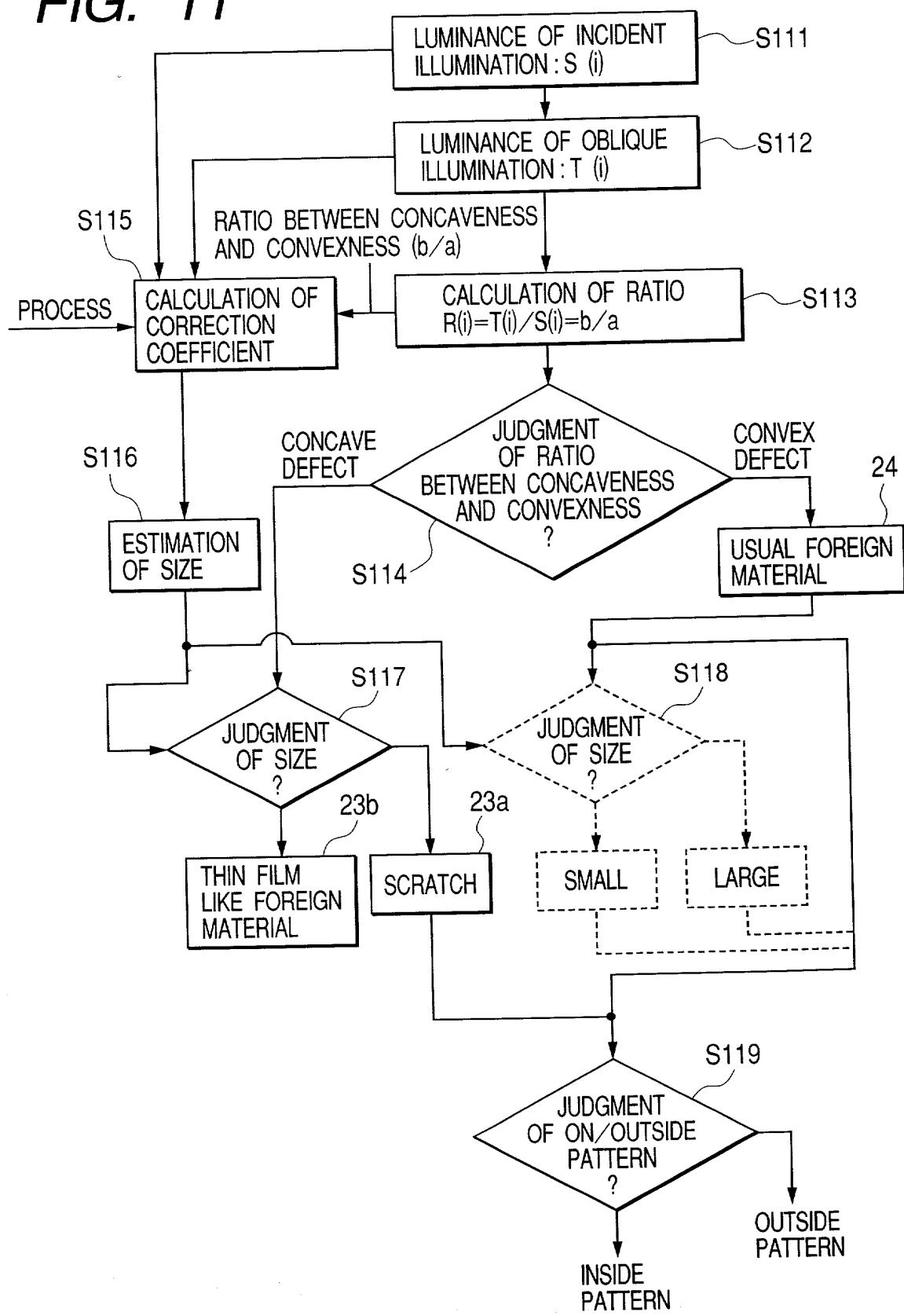


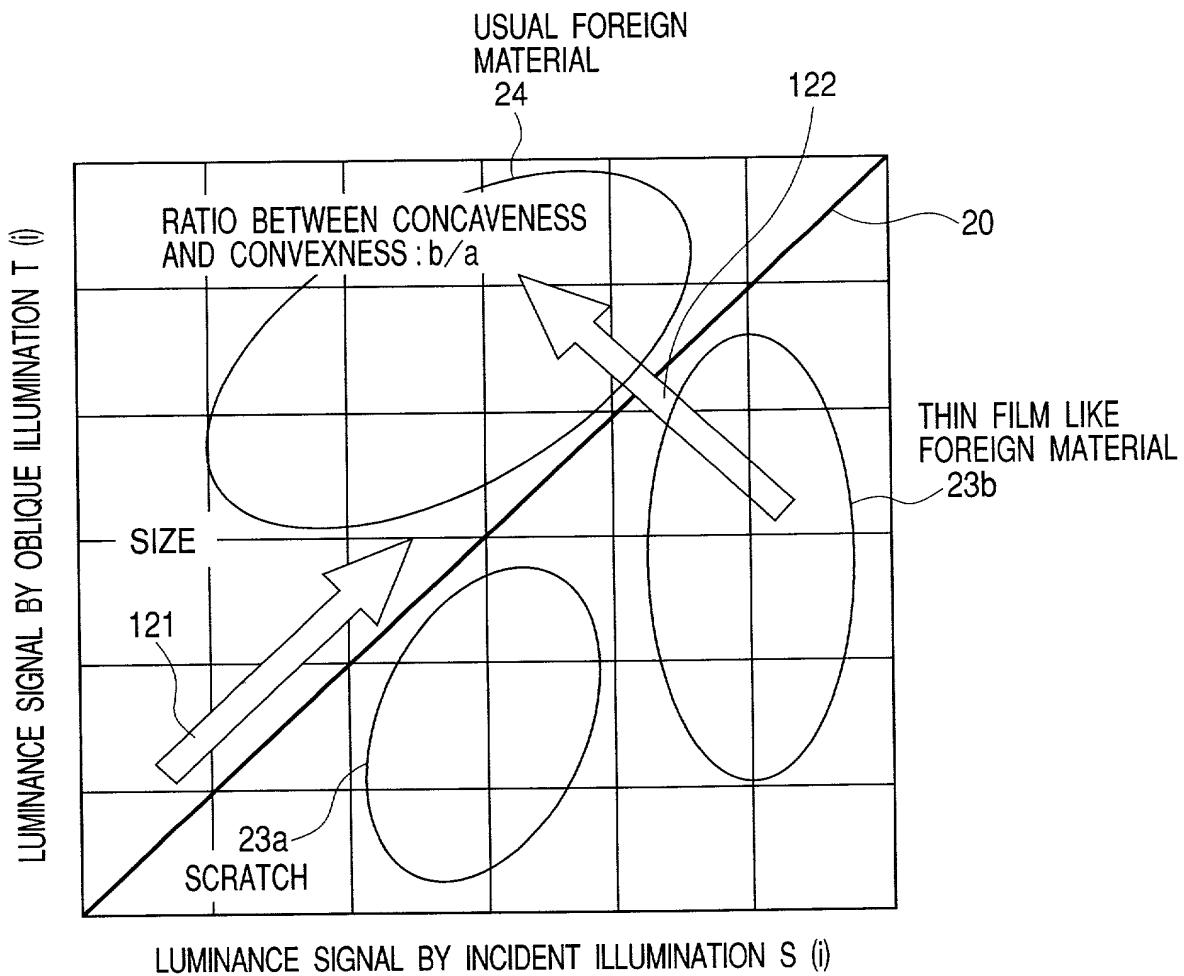
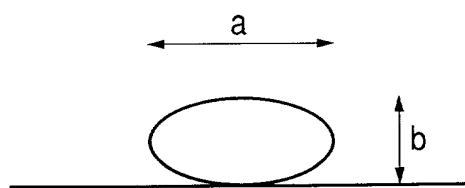
FIG. 12**FIG. 13**

FIG. 14

SIZE	SMALL		LARGE
FOREIGN MATERIAL/ SCRATCH	FOREIGN MATERIAL	SCRATCH	—
ON PATTERN	CATEGORY 1	CATEGORY 3	CATEGORY 5
OUTSIDE PATTERN	CATEGORY 2	CATEGORY 4	

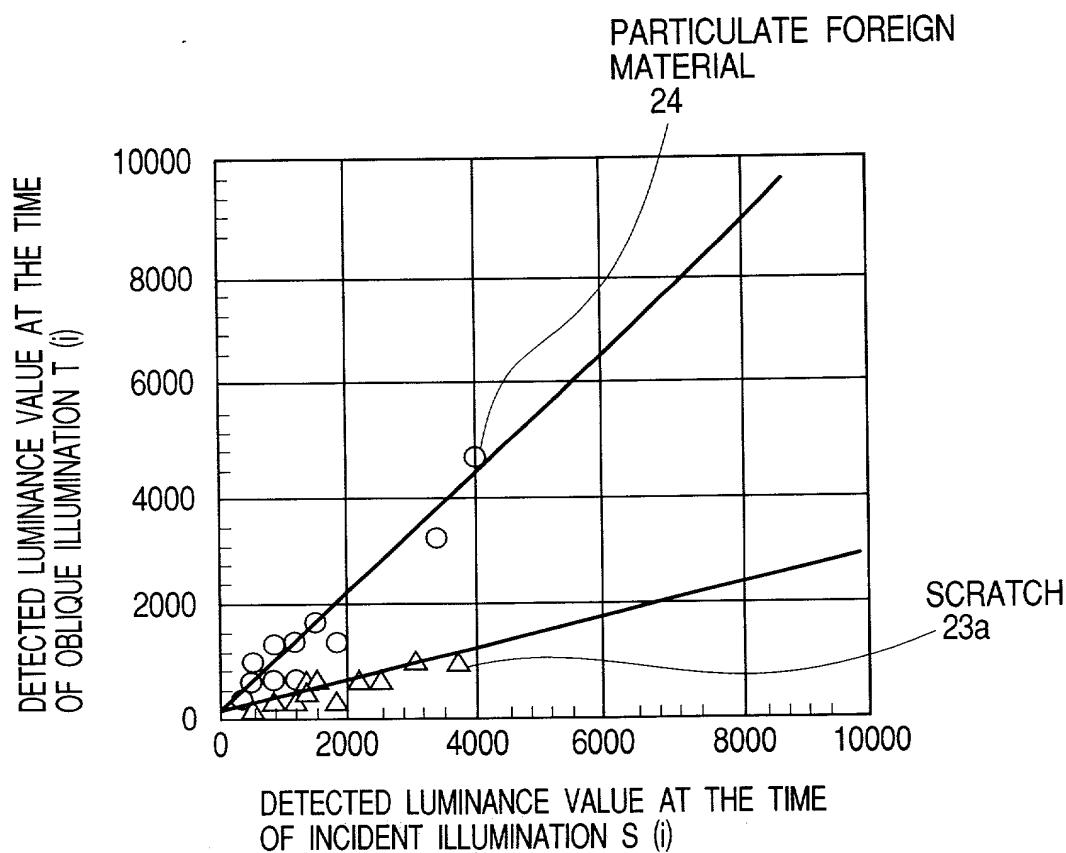
FIG. 15

FIG. 16

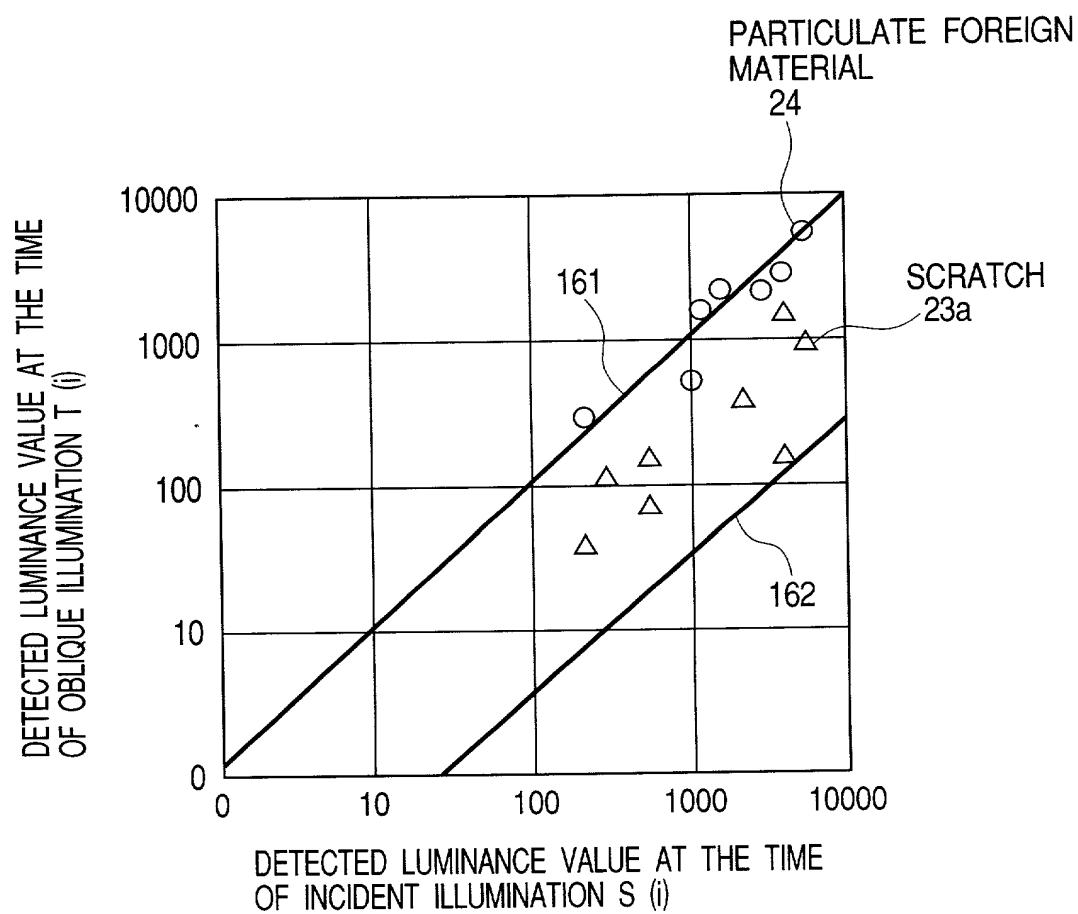


FIG. 17(a)

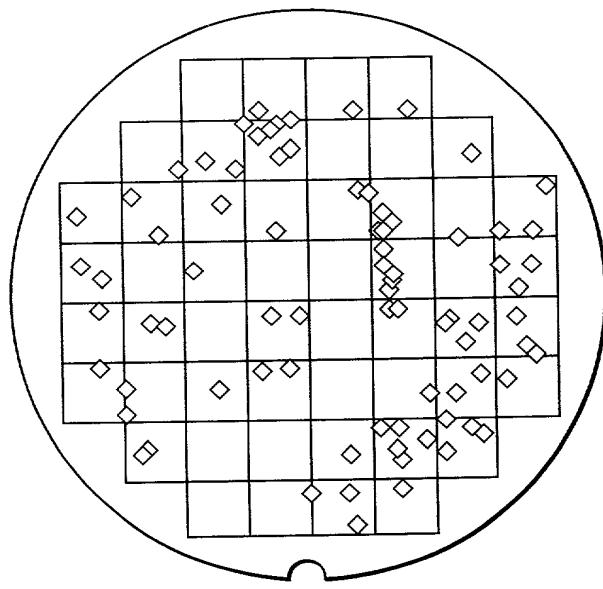


FIG. 17(b)

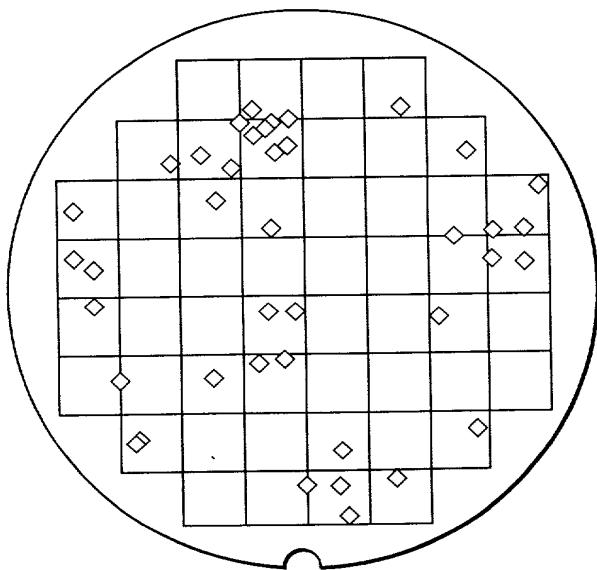


FIG. 17(c)

